English Translation of Amendments under PCT Article 34 filed on September 15, 2004

CLAIMS

1. (Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a current output terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current output terminal of the driving transistor; and

a third switching transistor for connecting the second terminal of the second capacitor to a predetermined voltage line.

2. (Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a current input terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current input terminal of the driving transistor; and

a third switching transistor for connecting the second terminal of the second capacitor to a predetermined voltage line.

3. (Amended) The display apparatus as set forth in claim 1, wherein:

during a first period within a current writing period of the driving transistor, the first switching transistor connects the current control terminal to the current output terminal, and the third switching transistor connects the second terminal to the predetermined voltage line,

during a second period within the current writing period, the first switching transistor disconnects the current control terminal from the current output terminal, and the third switching transistor disconnects the second terminal from the predetermined voltage line, and the

second switching transistor connects the second terminal to the current output terminal; and

during a readout period of the driving transistor, the second switching transistor disconnects the second terminal from the current output terminal, and the driving transistor supplies a current to the current light emitting element.

4. (Amended) The display apparatus as set forth in claim 2, wherein:

during a first period within a current writing period of the driving transistor, the first switching transistor connects the current control terminal to the current input terminal, and the third switching transistor connects the second terminal to the predetermined voltage line,

during a second period within the current writing period, the first switching transistor disconnects the current control terminal from the current input terminal, and the third switching transistor disconnects the second terminal from the predetermined voltage line, and the second switching transistor connects the second terminal to the current input terminal; and

during a readout period of the driving transistor, the second switching transistor disconnects the second terminal from the current input terminal, and the driving

transistor supplies a current to the current light emitting element.

5. (Amended) The display apparatus as set forth in any one of claims 1 through 4, wherein:

the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in each pixel circuit or each source driver circuit.

6. (Amended) The display apparatus as set forth in claim 5, wherein:

each of the source driver circuits includes the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor; and

each of the pixel circuits includes a transistor for controlling a current that is to be supplied to the current driving light emitting element.

7. (Amended) The display apparatus as set forth in any one of claims 1 through 4, wherein:

one or more of the first capacitor, the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor

are provided in a pixel circuit, and the others are provided in a portion outside the pixel circuit, which portion includes a source driver circuit.

8. (Amended) The display apparatus as set forth in claim 7, wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit; and

the second capacitor, the first switching transistor, the second switching transistor, and the third switching transistor are provided in the portion outside the pixel circuit, which portion includes the source driver circuit,

said display apparatus, further comprising:

a connecting wire for connecting the current control terminal of the driving transistor to the first terminal of the second capacitor.

9. (Amended) The display apparatus as set forth in claim 8,

wherein:

the current driving light emitting element, the driving transistor, and the first capacitor are provided in the pixel circuit;

the second capacitor, the first switching transistor

are provided outside the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to the second switching transistor and the third switching transistor.

10. (Amended) The display apparatus as set forth in claim 7,

wherein:

the current driving light emitting element, the driving transistor, the first switching transistor, the first capacitor, and the second capacitor are provided in the pixel circuit; and

the second switching transistor and the third switching transistor are provided in the source driver circuit or the portion outside the pixel circuit;

the display apparatus further comprising:

a connecting wire for connecting the second terminal of the second capacitor to (i) the current output terminal of the driving transistor, or (ii) the current input terminal of the driving transistor.

11. (Amended) The display apparatus as set forth in

claim 8 or 10, further comprising:

an OFF potential line for supplying an OFF potential;

wherein:

the connecting wire is connected to the OFF potential line via a fourth switching transistor.

12. (Amended) A method for driving a display apparatus including a current driving light emitting element and a driving transistor, the method comprising the steps of:

electrically connecting a current control terminal of the driving transistor to a first terminal of a first capacitor;

electrically connecting, during a current writing period of the driving transistor, the first terminal of the first capacitor to a first terminal of a second capacitor;

during a first period, (i) electrically connecting a second terminal of the second capacitor to a predetermined voltage line, and (ii) electrically connecting the current control terminal of the driving transistor to a current output terminal of the driving transistor, and (iii) causing the first capacitor and the second capacitor to retain a current control terminal potential that the driving transistor has on this occasion;

during a second period, (i) correcting the current control terminal potential by disconnecting the current control terminal of the driving transistor from the current output terminal of the driving transistor, and by changing electric connection of the second terminal of the second capacitor from the predetermined voltage line to the current output terminal of the driving transistor, and (ii) causing the first capacitor to retain the current control terminal potential that the driving transistor has on this occasion; and

controlling, during a current readout period of the driving transistor, an output current of the driving transistor with the use of the current control terminal potential, retained by the first capacitor, of the driving transistor.

13. (Added) A method for driving a display apparatus including a current driving light emitting element and a driving transistor, the method comprising the steps of:

electrically connecting a current control terminal of the driving transistor to a first terminal of a first capacitor;

electrically connecting, during a current writing period of the driving transistor, the first terminal of the

first capacitor to a first terminal of a second capacitor;

during a first period, (i) electrically connecting a second terminal of the second capacitor to a predetermined voltage line, and (ii) electrically connecting the current control terminal of the driving transistor to a current input terminal of the driving transistor, and (iii) causing the first capacitor and the second capacitor to retain a current control terminal potential that the driving transistor has on this occasion;

during a second period, (i) correcting the current control terminal potential by disconnecting the electric connection between the current control terminal of the driving transistor and the current input terminal of the driving transistor, and by changing electric connection of the second terminal of the second capacitor from the predetermined voltage line to the current input terminal of the driving transistor, and (ii) causing the first capacitor to retain the current control terminal potential that the driving transistor has on this occasion; and

controlling, during a current readout period of the driving transistor, an input current of the driving transistor with the use of the current control terminal potential, retained by the first capacitor, of the driving transistor.

14. (Added) The driving method as set forth in claim 12 or 13, wherein:

during the second period, the electric connecting of the second terminal of the second capacitor to the current output terminal of the driving transistor is carried out before disconnecting the predetermined voltage line from the second terminal of the second capacitor.

English Translation of Amendments under PCT Article 34 filed on December 20, 2004

CLAIMS

1. (Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a current output terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current output terminal of the driving transistor via a wire or a transistor; and

a third switching transistor for connecting the second terminal of the second capacitor to a predetermined voltage line.

2. (Amended) A display apparatus including a current driving light emitting element and a driving transistor, the display apparatus comprising:

a first switching transistor for connecting (i) a current control terminal of the driving transistor to (ii) a

current input terminal of the driving transistor;

a first capacitor, connected to the current control terminal of the driving transistor;

a second capacitor, having a first terminal connected to the current control terminal of the driving transistor;

a second switching transistor for connecting a second terminal of the second capacitor to the current input terminal of the driving transistor via a wire and a transistor; and

a third switching transistor for connecting the second terminal of the second capacitor to a predetermined voltage line.

3. The display apparatus as set forth in claim 1, wherein:

during a first period within a current writing period of the driving transistor, the first switching transistor connects the current control terminal to the current output terminal, and the third switching transistor connects the second terminal to the predetermined voltage line,

during a second period within the current writing period, the first switching transistor disconnects the current control terminal from the current output terminal,

and the third switching transistor disconnects the second terminal from the predetermined voltage line, and the